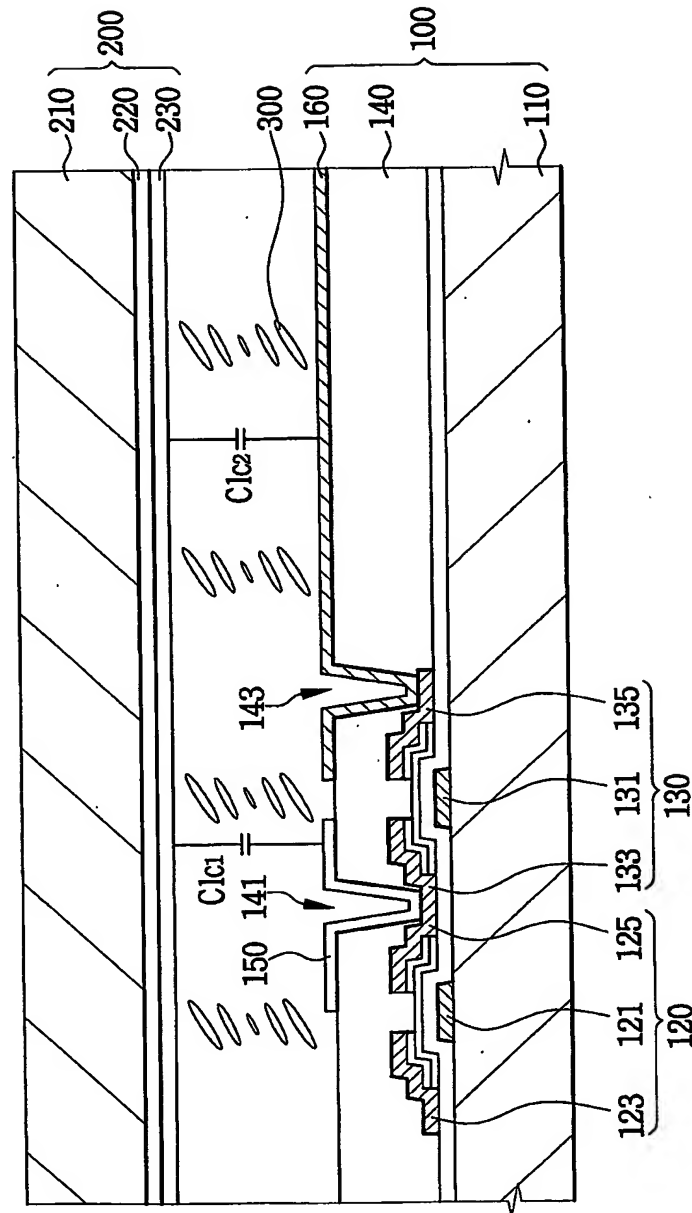
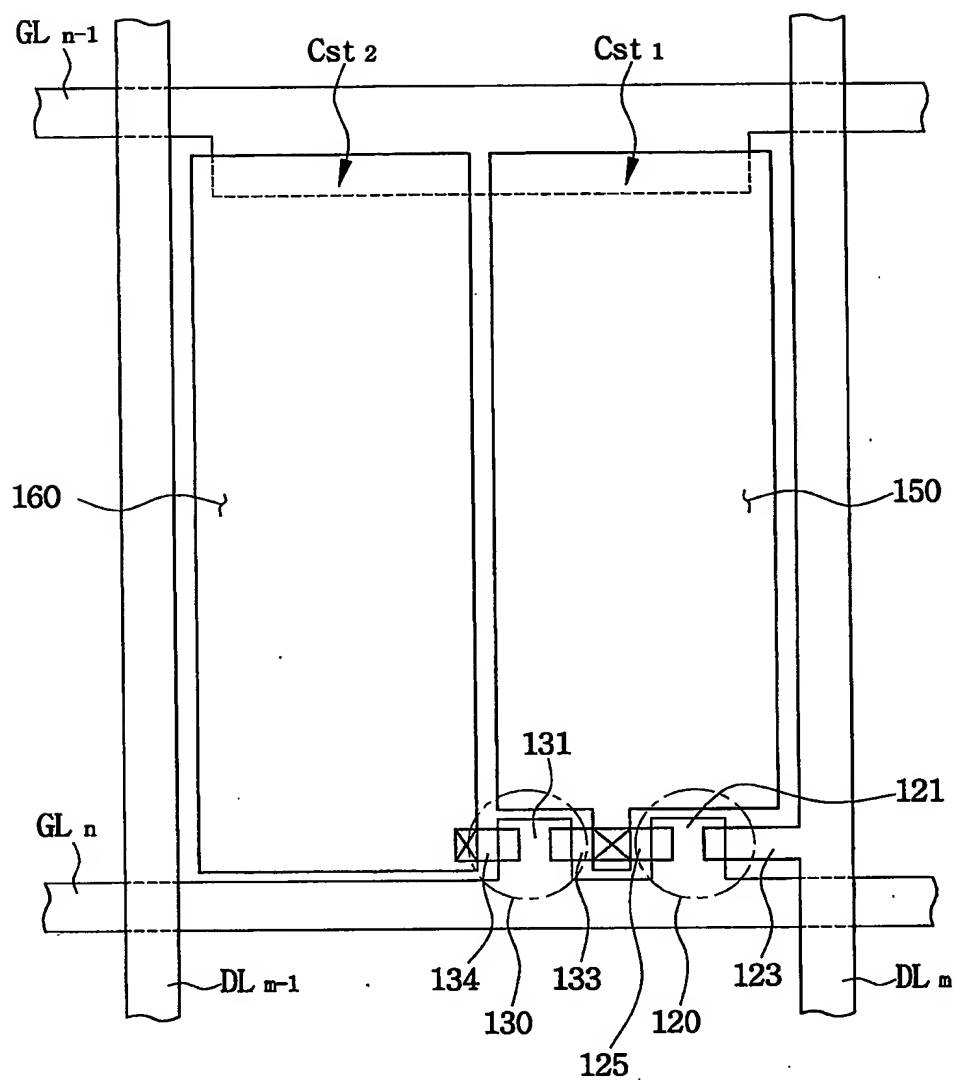


FIG. 2

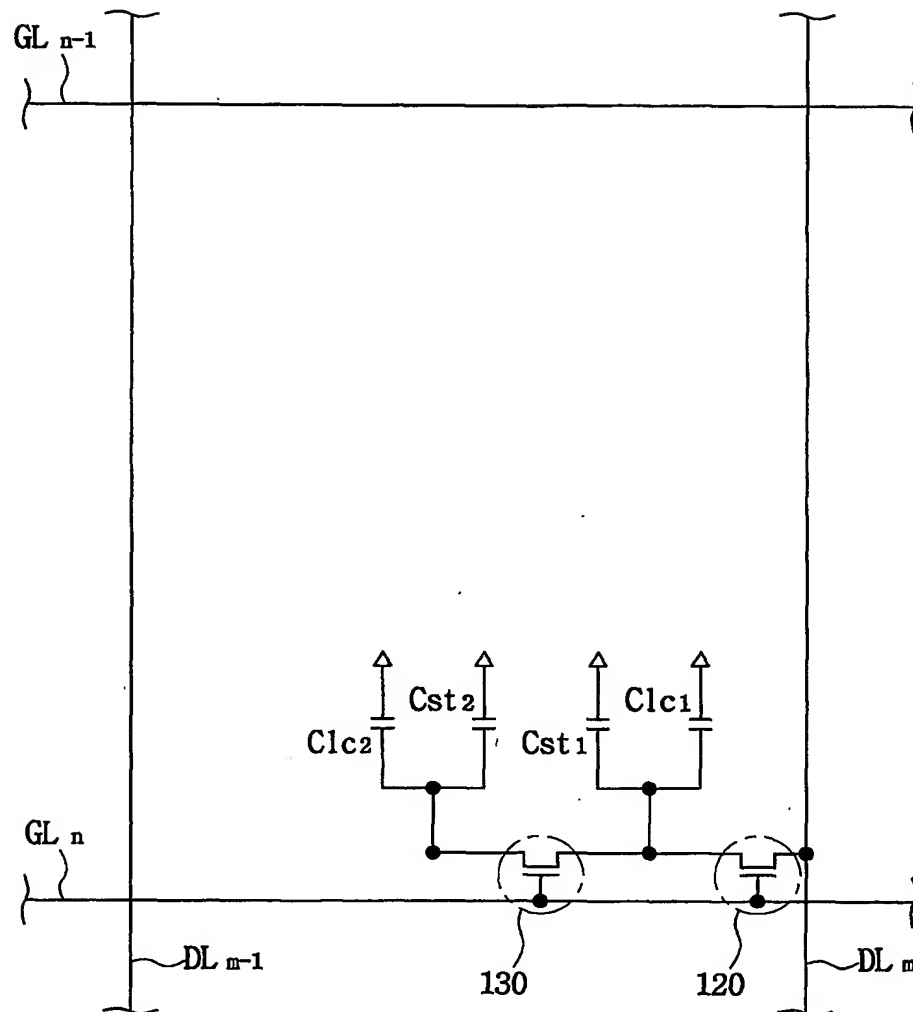
400



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FIG. 3

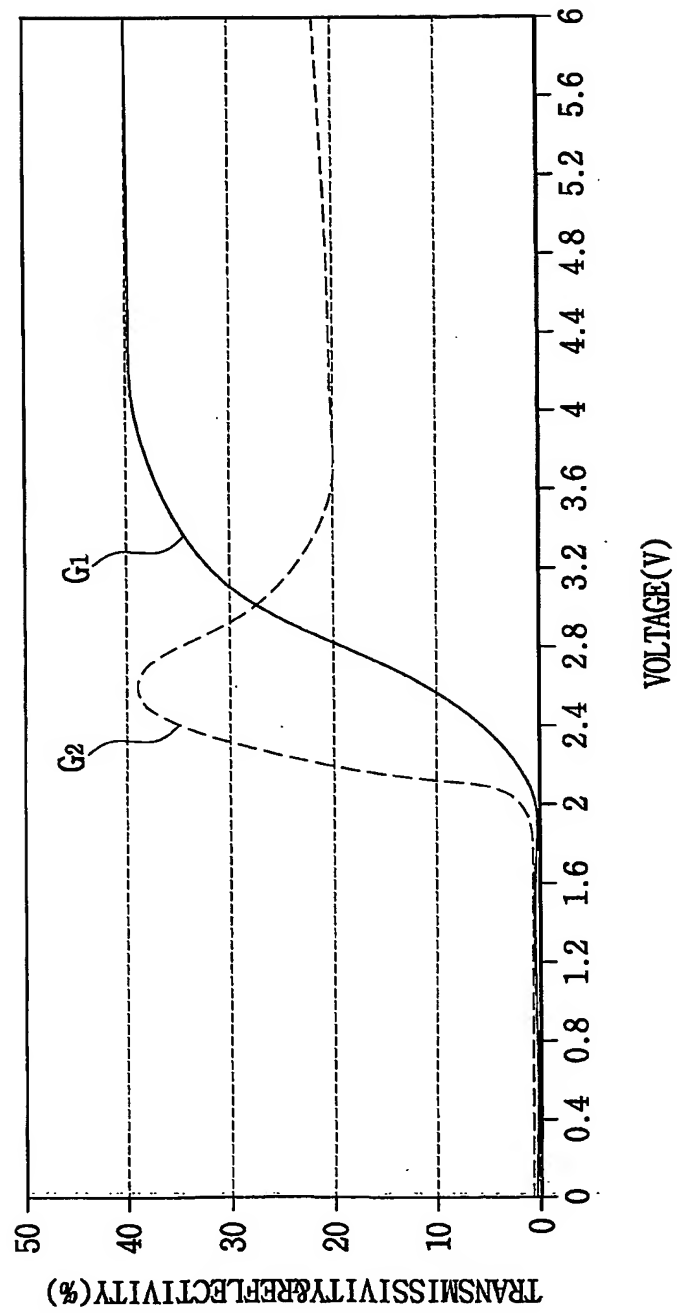
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FIG. 4



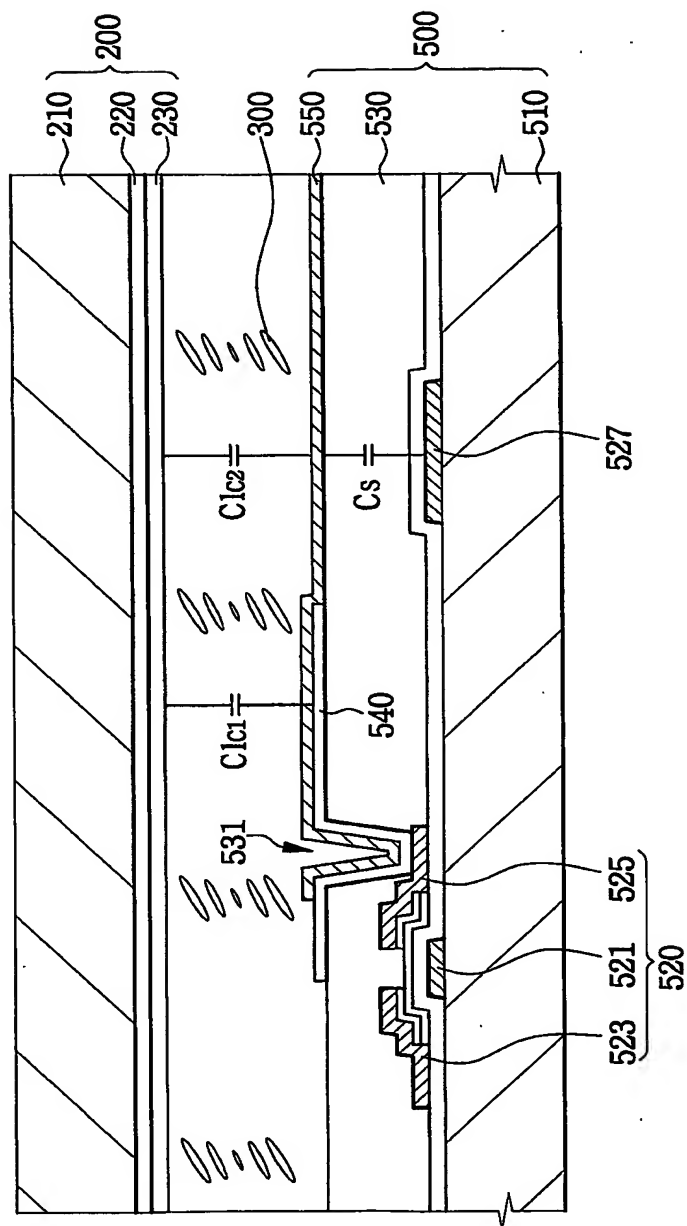
5/23

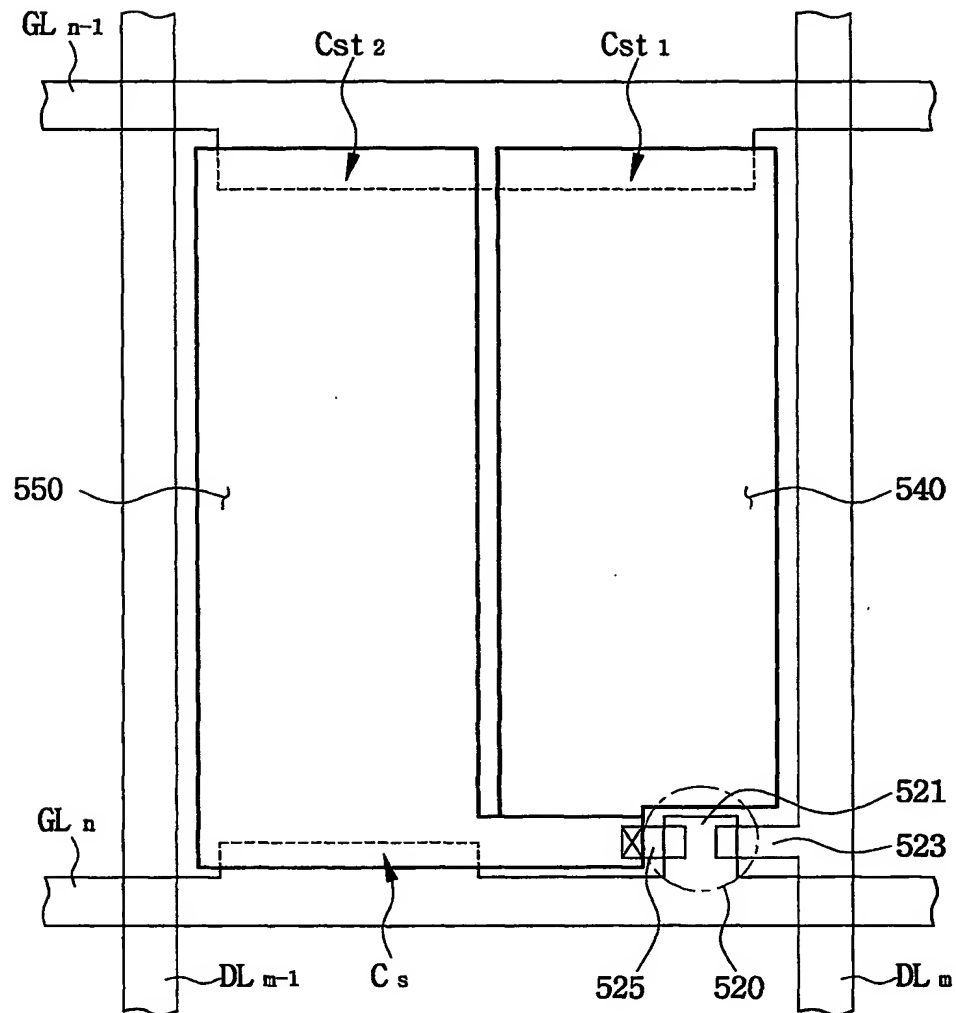
FIG. 5



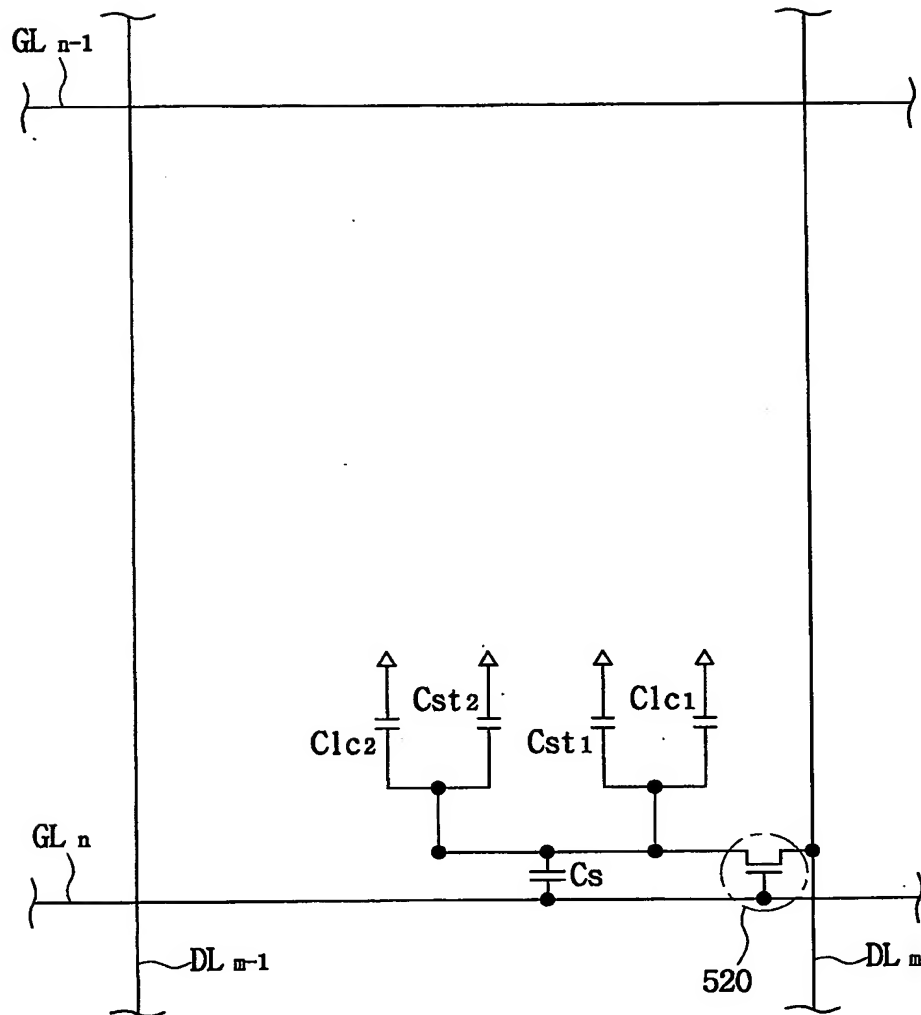
6/23

FIG. 6

600

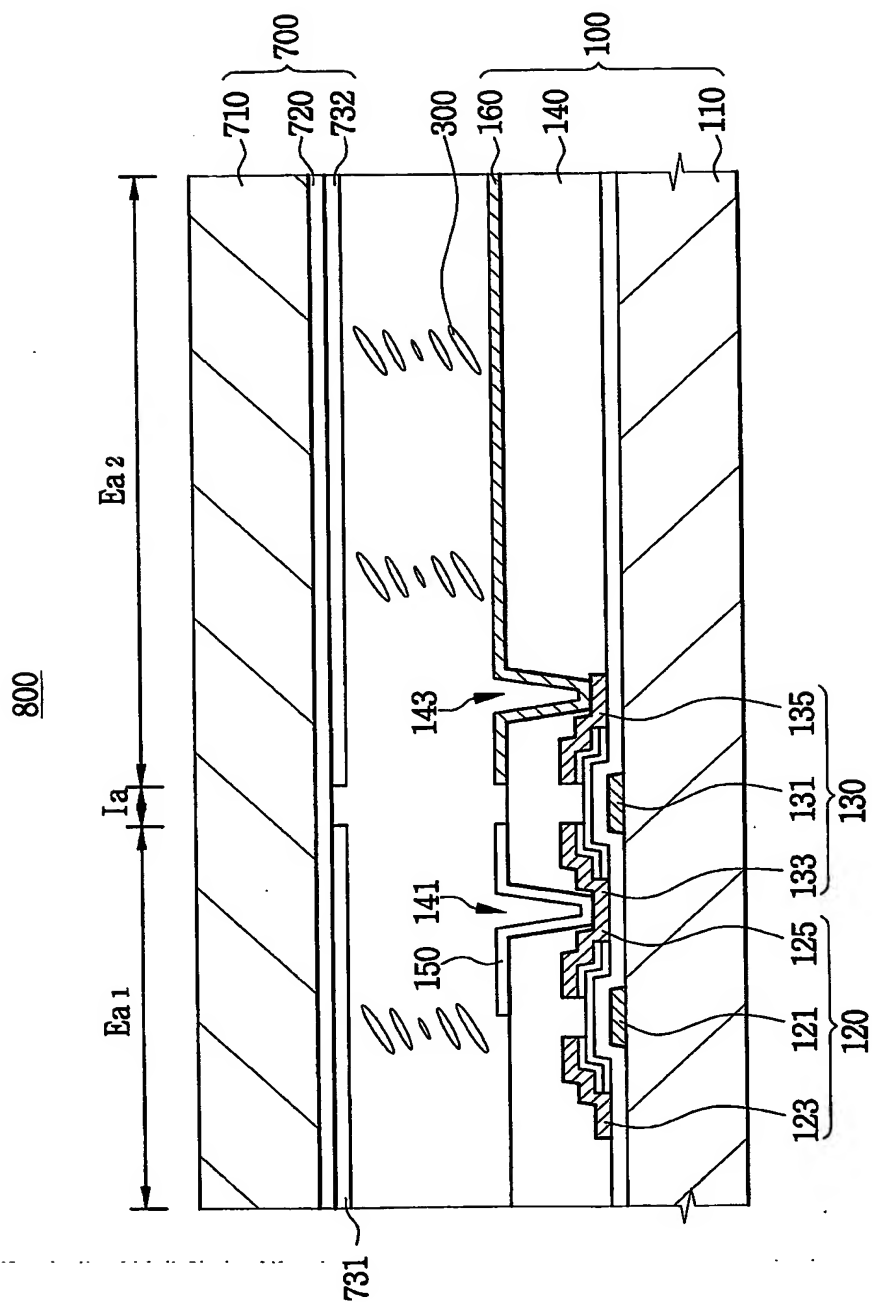
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FIG. 7

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FIG. 8

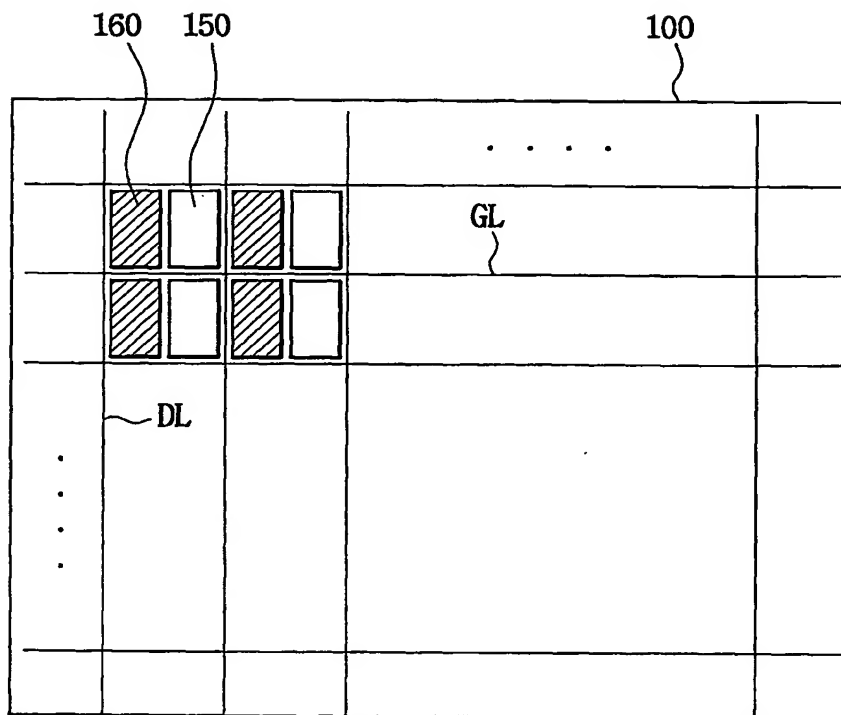


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FIG. 9



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FIG. 10



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FIG. 11

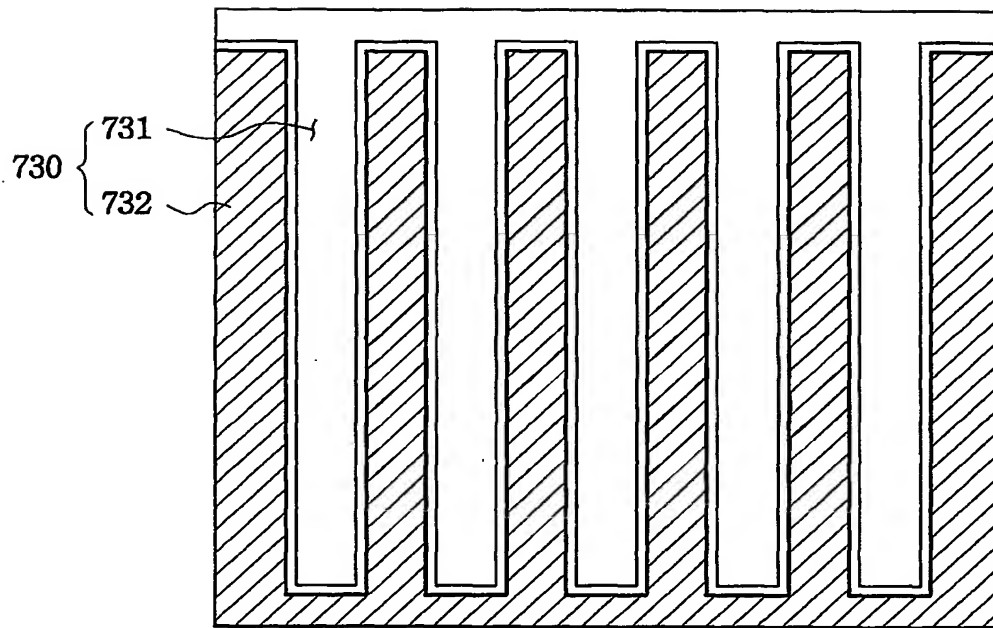
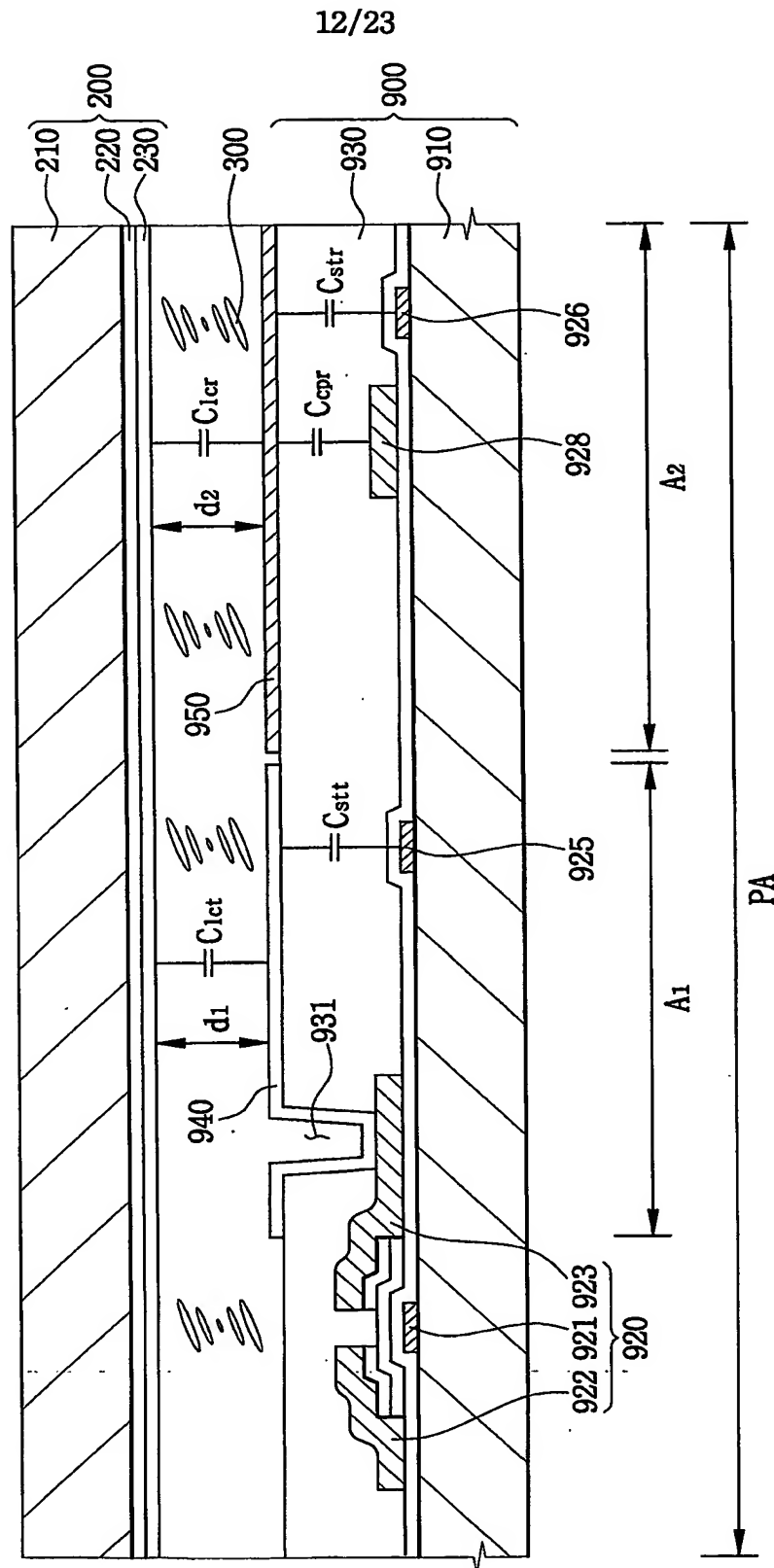


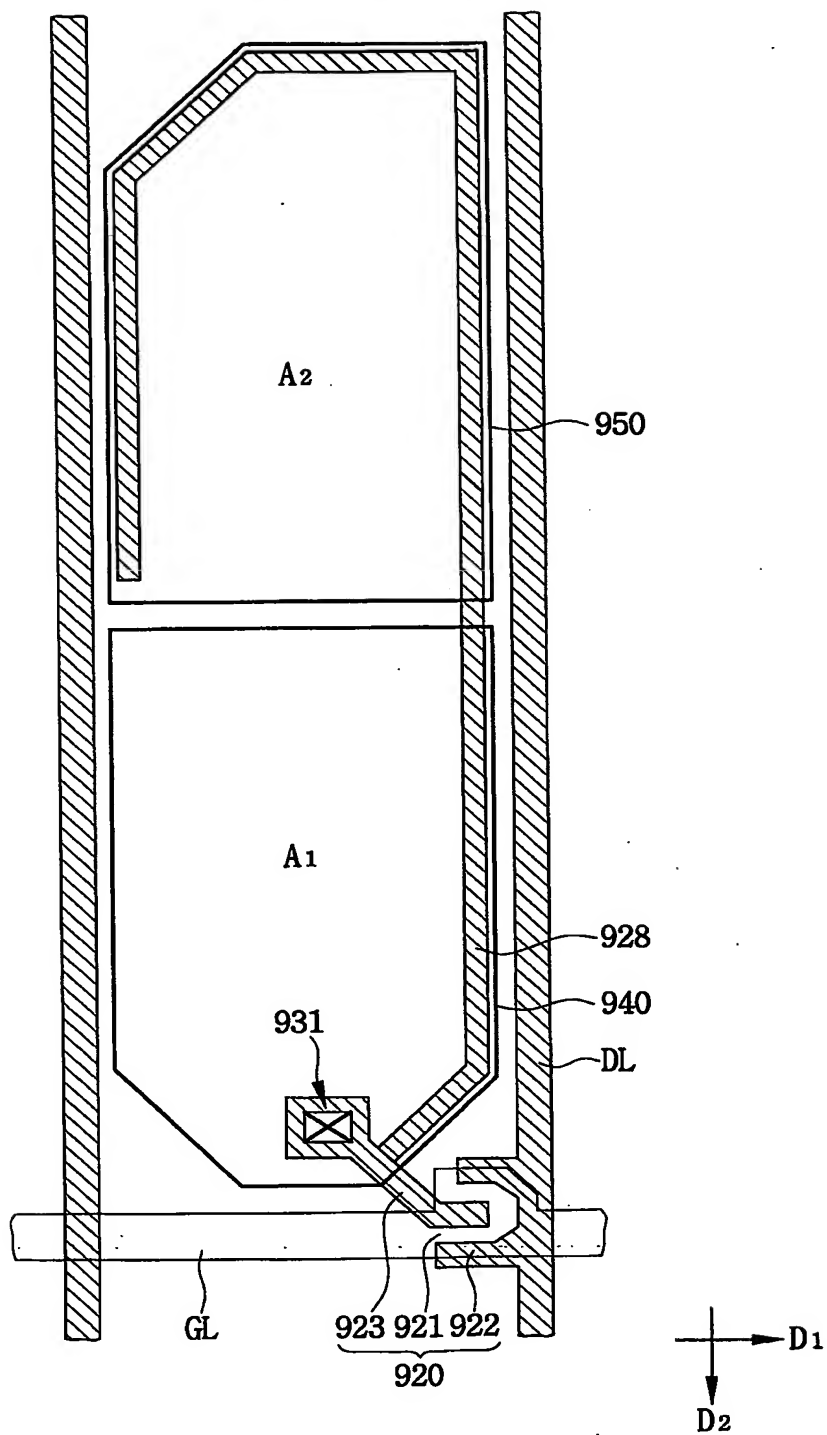
FIG. 12

1000



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FIG. 13

900



The diagram shows a memory cell 920. It is connected to a word line (DL) and a bit line (GL). The circuit includes several capacitors: C1ct, C1cr, Cstt, Ccpr, and Cstr. A dashed circle highlights the access transistor and its connection to the bit line (GL).

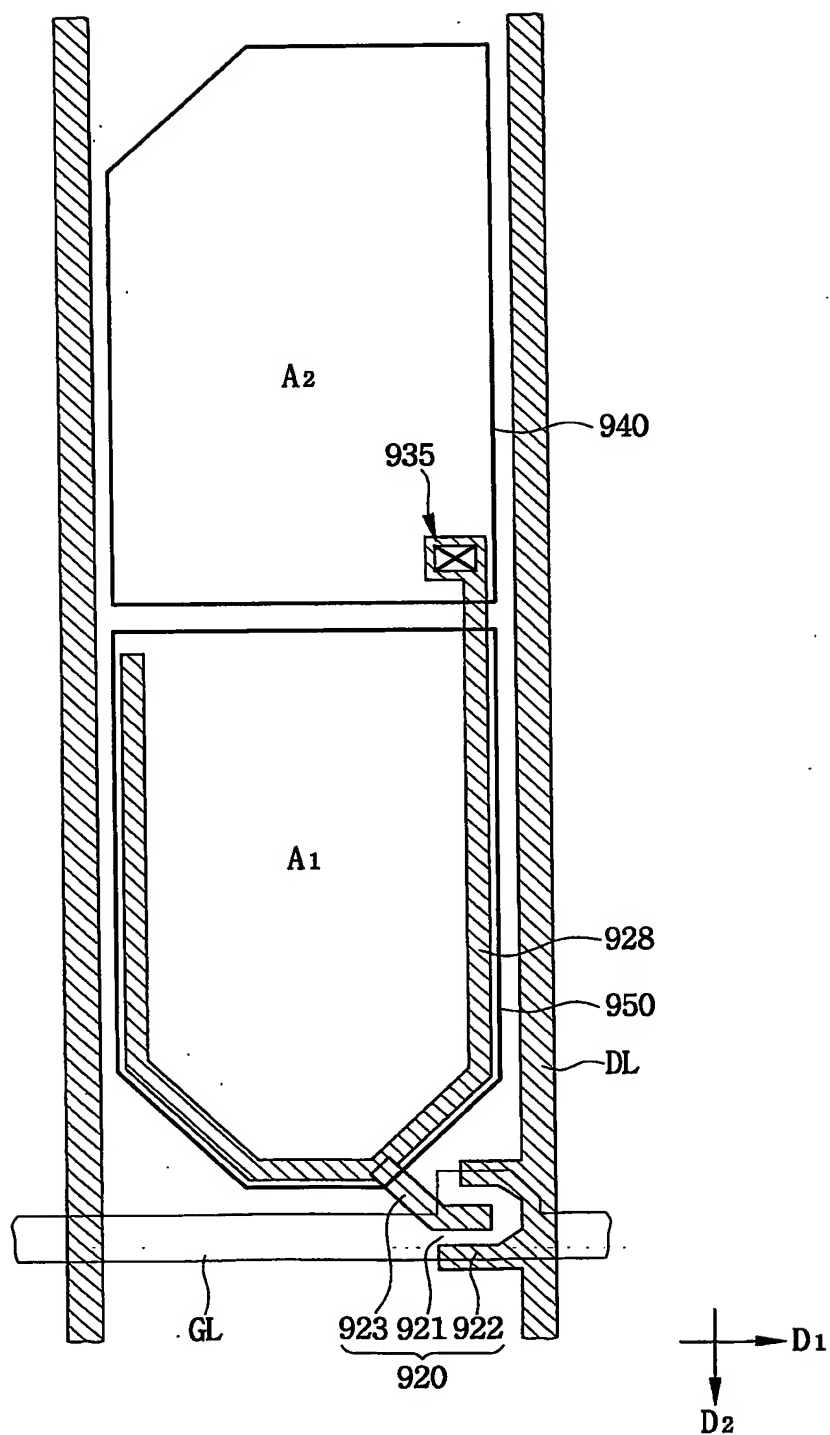
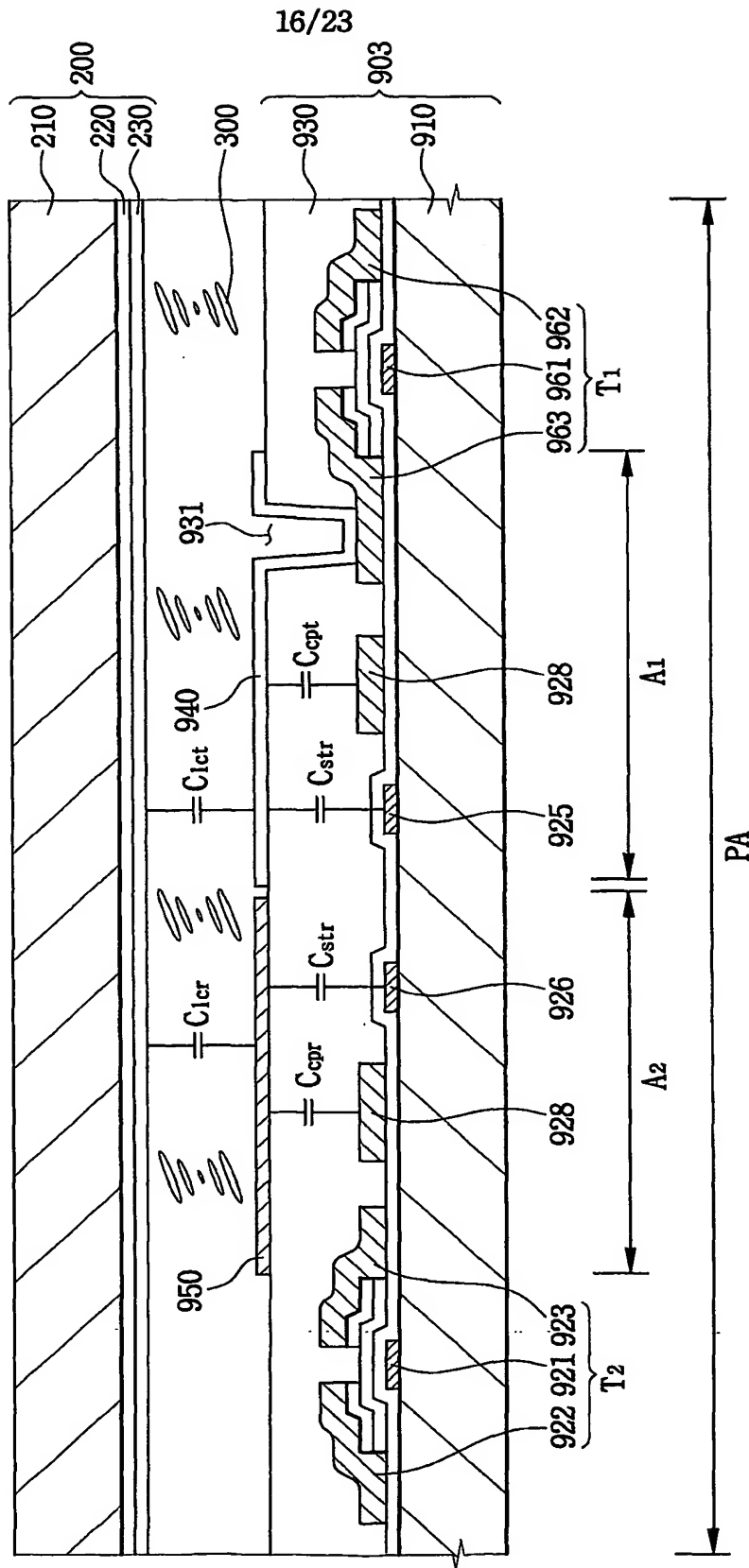
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FIG. 15901

FIG. 16

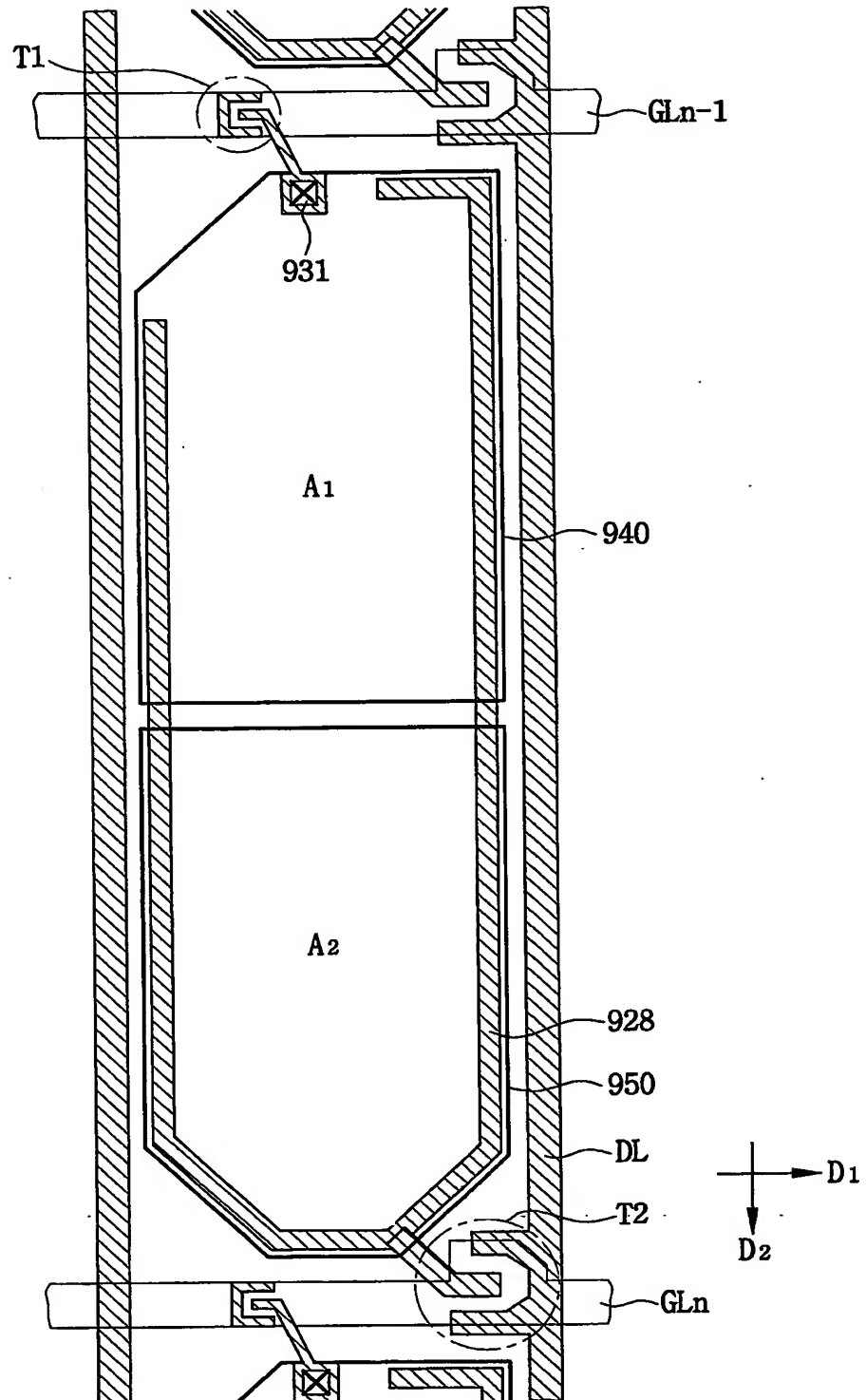
1100

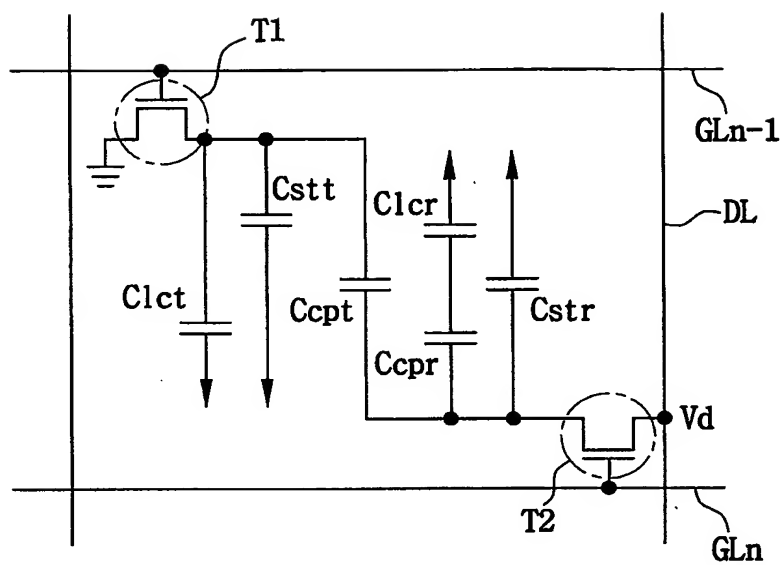


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FIG. 17

903

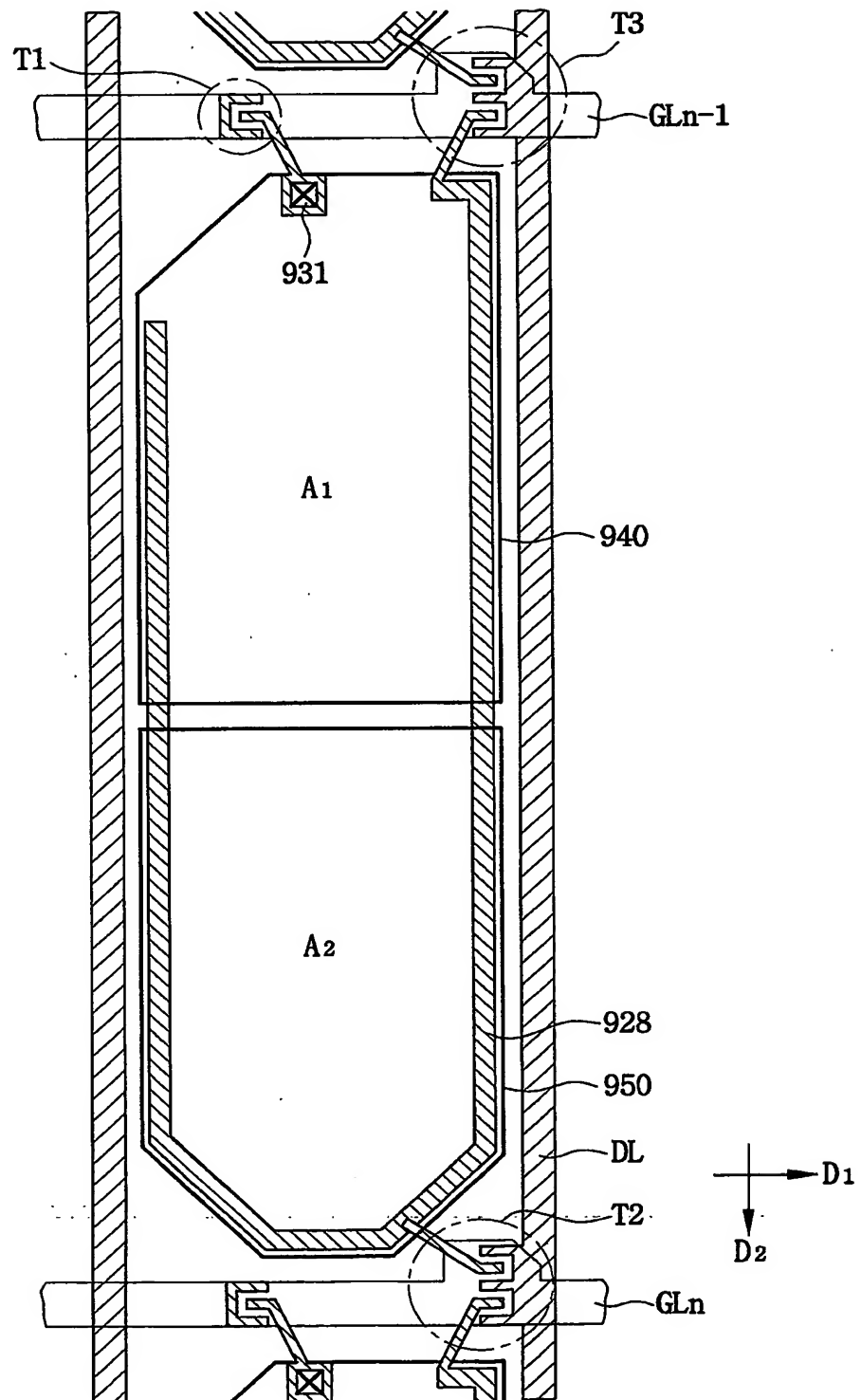


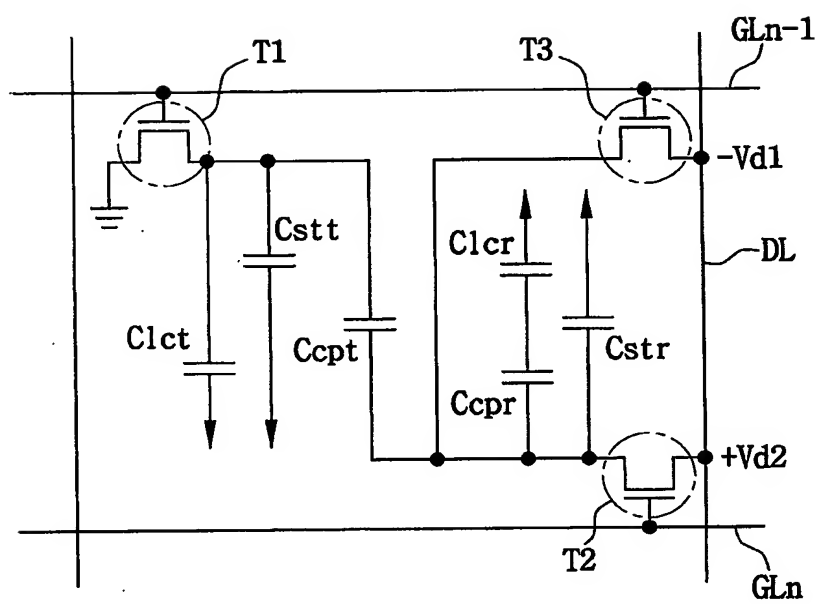
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FIG. 18

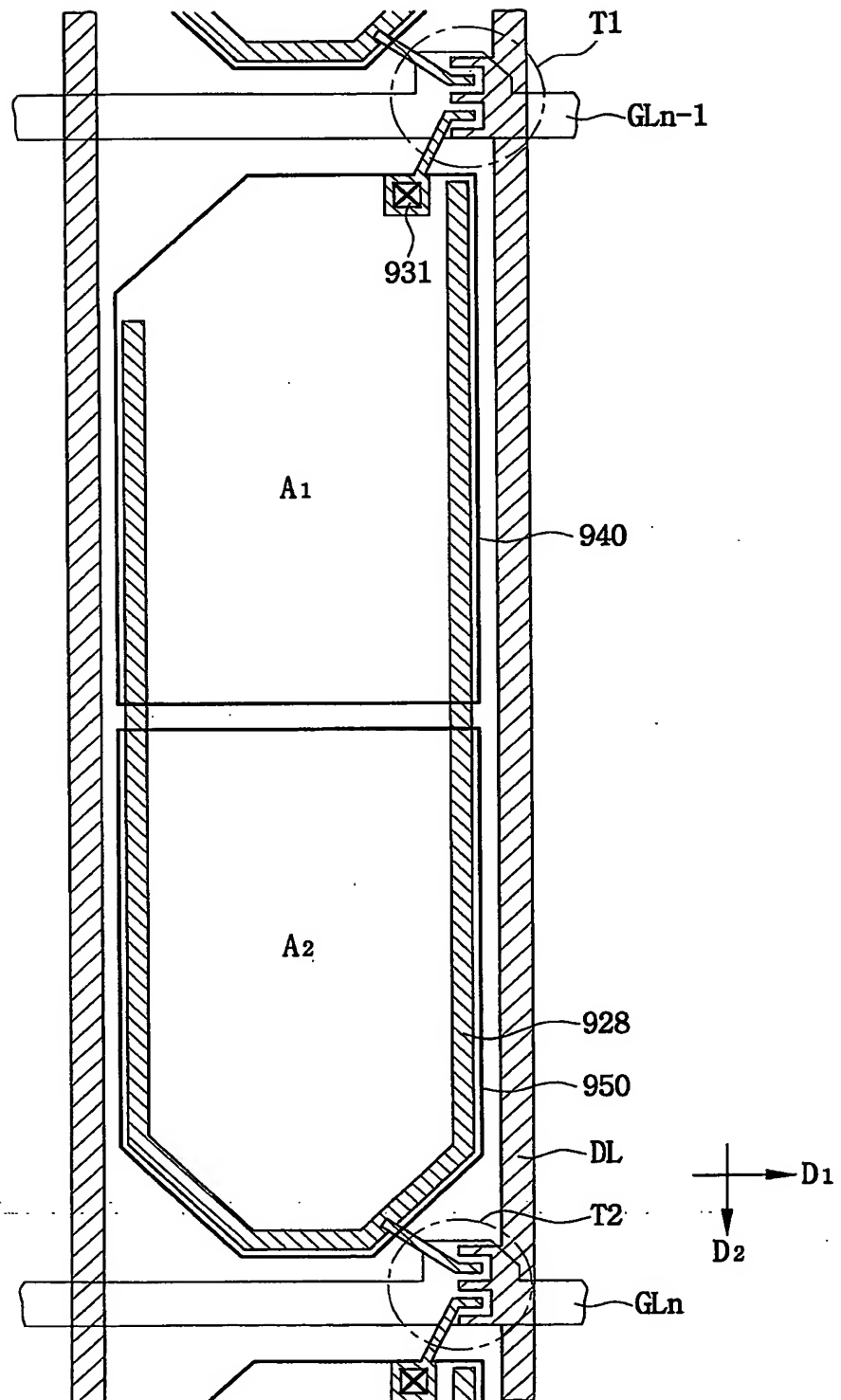
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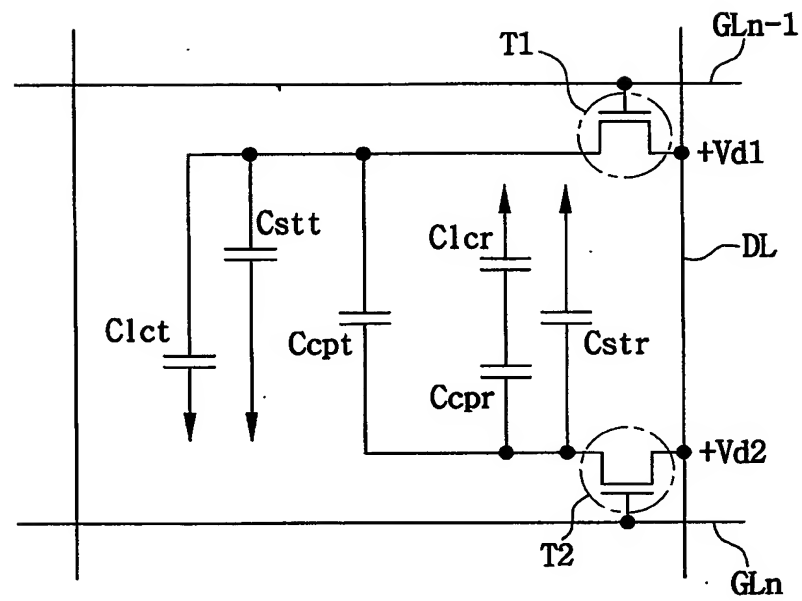
FIG. 19

905





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FIG. 21907

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FIG. 22

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FIG. 23

